

**UNITED STATES PATENT AND TRADEMARK OFFICE**  
(Case No. 02-1059-A)

In the Application of:	)	
	)	Group Art Unit: TBD
Gang Xue et al	)	
	)	Examiner: Not Yet Assigned
Serial No. TBD	)	
	)	
Filed: October 7, 2003	)	
	)	
For: Single Bit Nonvolatile Memory Cell	)	
and Methods for Programming and Erasing	)	
Thereof	)	

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to the duty of disclosure provided by 35 C.F.R. § 1.56 and §§ 1.97-98, the applicants wish to make the following references and information of record in the above-identified application. The references are also listed in the PTO-1449 form enclosed herewith. It is requested that the references and information be given careful consideration and that they be cited of record in the prosecution history of the present application so that they will appear on the face of the patent issuing from the present application.

In the judgment of the undersigned, portions of the references may be material to the examination of the pending claims. However, the references have not been reviewed in sufficient detail to make any other representation and, in particular, no representation is intended as to the relative importance of any portion of the references. This Statement is not a representation that the

cited references have effective dates early enough to be "prior art" within the meaning of 35 U.S.C. §§ 102 or 103.

#### CITED REFERENCES

##### Foreign Patents:

1. European Patent EP 1 096 572 A1, van Houdt, et al., issued October 25, 2000.

##### Other Documents:

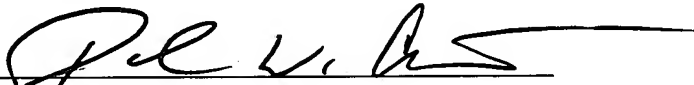
1. "CHISEL Flash EEPROM—Part I: Performance and Scaling", Souvik Mahapatra, S. Shukuri, Jeff Bude, IEEE Transactions On Electron Devices, Vol. 49, No. 7, July 2002.

Respectfully submitted,

McDonnell Boehnen Hulbert & Berghoff


Date: Oct. 7, 2003

By:

  
Paul W. Churilla  
Reg. No. 47,495

#### CERTIFICATE OF MAILING

The undersigned hereby certifies that the foregoing INFORMATION DISCLOSURE STATEMENT is being deposited with the United States Postal Service with sufficient postage as Express Mail, Mailing Number EK 745543142 US in an envelope addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. 1450, Alexandria, VA 22313-1450, on this 7 day of October, 2003.

  
Paul W. Churilla

FORM PTO-1449  
(Rev. 2-32)

U.S. Department of Commerce  
Patent and Trademark Office

INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

(Use several sheets if necessary)

Atty. Docket No.

02-1059-A

Serial No.

TBD

Applicant:

Gang Xue et al.

Filing Date:

10/07/03

Group:

TBD

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate

FOREIGN PATENT DOCUMENTS

		Document Number							Date	Country	Class	Subclass	Translation	
													Yes	No
		EP	1	0	96	5	7	2A1	10/25/00	EP				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc).

			"CHISEL Flash EEPROM—Part I: Performance and Scaling", Souvik Mahapatra, S. Shukuri, Jeff Bude, IEEE Transactions On Electron Devices, Vol. 49, No. 7, July 2002.
EXAMINER			DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.